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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Kazushi HIGASHI et al.

Serial No. NEW

Filed August 29, 2001

METHOD FOR ASSEMBLING INTEGRAL  
TYPE ELECTRONIC COMPONENT AND  
INTEGRAL TYPE ELECTRONIC COMPONENT

Attn: Application Branch  
Attorney Docket No. 2001\_1194A

THE COMMISSIONER IS AUTHORIZED  
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CLAIM OF PRIORITY UNDER 35 USC 119

Assistant Commissioner for Patents,  
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Sir:

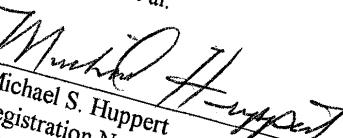
Applicants in the above-entitled application hereby claim the date of priority under the International Convention of Japanese Patent Application No. 2000-259131, filed August 29, 2000, as acknowledged in the Declaration of this application.

A certified copy of said Japanese Patent Application is submitted herewith.

Respectfully submitted,

Kazushi HIGASHI et al.

By

  
Michael S. Huppert  
Registration No. 40,268  
Attorney for Applicants

MSH/kjf  
Washington, D.C. 20006-1021  
Telephone (202) 721-8200  
Facsimile (202) 721-8250  
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## ARTIFACT SHEET

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